

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S2	2715	delay clock signal	US-PGPUB; USPAT	ADJ	ON	2007/10/17 13:08
S3	2840	(generat\$3 or produc\$3) same(delay or delayed) clock signal	US-PGPUB; USPAT	ADJ	ON	2007/10/17 13:09
S4	2840	(generat\$3 or produc\$3) same (delay or delayed) clock signal	US-PGPUB; USPAT	ADJ	ON	2007/10/17 13:09
S5	2672	327/110,427,172.ccls.	US-PGPUB; USPAT	ADJ	ON	2007/12/17 15:12
S6	647	327/110,427,172.ccls. and clock	US-PGPUB; USPAT	ADJ	ON	2007/12/17 15:13
S7	650	327/110,427,172.ccls. and (clock or clk)	US-PGPUB; USPAT	ADJ	ON	2007/12/17 15:13
S8	187	327/110,427,172.ccls. and (clock or clk) and latch	US-PGPUB; USPAT	ADJ	ON	2007/12/17 15:35
S9	96	327/110,427,172.ccls. and (clock or clk) and capacitor and latch	US-PGPUB; USPAT	ADJ	ON	2007/12/17 15:44
S10	71	327/110,427,172.ccls. and (clock or clk) and capacitor and latch and delay	US-PGPUB; USPAT	ADJ	ON	2007/12/17 15:45
S11	920	327/427.ccls.	US-PGPUB; USPAT	ADJ	ON	2007/12/17 16:04
S12	54	S11 and latch and capacitor	US-PGPUB; USPAT	ADJ	ON	2007/12/17 16:05
S13	1	S11 and master latch and capacitor	US-PGPUB; USPAT	ADJ	ON	2007/12/17 16:28

S14	224	master latch and capacitor and delay	US-PGPUB; USPAT	ADJ	ON	2007/12/17 16:29
S15	261	master latch and slave latch and capacitor	US-PGPUB; USPAT	ADJ	ON	2007/12/18 15:51
S16	3035	master near3 slave same latch	US-PGPUB; USPAT	ADJ	ON	2007/12/19 07:42
S17	741	master near3 slave same latch and "327".clas.	US-PGPUB; USPAT	ADJ	ON	2007/12/19 07:42
S18	165	master near3 slave same latch and "327".clas. and capacitor	US-PGPUB; USPAT	ADJ	ON	2007/12/19 07:53
S19	1966	latch and flip near2 flop and "327".clas. and capacitor	US-PGPUB; USPAT	ADJ	ON	2007/12/19 08:30
S20	329	latch and flip near2 flop and "327".clas. and capacitor and (pass gate or clocked inverter or transmission gate)	US-PGPUB; USPAT	ADJ	ON	2007/12/19 08:31
S21	11	"6140848"	US-PGPUB; USPAT	ADJ	ON	2007/12/19 11:28
S22	0	2005/0206436	US-PGPUB; USPAT	ADJ	ON	2007/12/19 15:22
S23	2	"20050206436"	US-PGPUB; USPAT	ADJ	ON	2007/12/19 15:23
S24	650	master near3 latch and capacitor	US-PGPUB; USPAT	ADJ	ON	2007/12/31 07:46
S25	0	master near3 latch and capacitor and "323,327". clas.	US-PGPUB; USPAT	ADJ	ON	2007/12/31 08:15
S26	165	master near3 latch and capacitor and "327".clas.	US-PGPUB; USPAT	ADJ	ON	2007/12/31 08:15
S27	2901	(switch or switches) near3 series and "327".clas.	US-PGPUB; USPAT	ADJ	ON	2008/01/02 16:01

S28	180	plurality near3 switches near3 series and "327". clas.	US-PGPUB; USPAT	ADJ	ON	2008/01/02 16:04
S29	116	plurality near3 switches near3 series and "327". clas. and capacitor	US-PGPUB; USPAT	ADJ	ON	2008/01/02 16:05
S31	251	326/93-98.ccls. and (clock or clk) and capacitor and (domino or dynamic)	US-PGPUB; USPAT	ADJ	ON	2008/01/04 08:51
S32	1	("7239181").PN.	USPAT; USOCR	OR	OFF	2008/01/04 15:18
S33	0	(2002/0075039).CCLS.	USPAT; USOCR	OR	OFF	2008/01/04 17:10
S34	4	((("0002002") or ("0075039")).PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2008/01/04 17:10
S35	1	("20020075039").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2008/01/04 17:10
S36	1	("20020075039").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2008/01/07 08:39
S37	7063	double near2 data near2 rate and DRAM	US-PGPUB; USPAT	OR	ON	2008/01/07 09:33
S38	225	double near2 data near2 rate and (DRAM.ti. (dynamic near2 random near2 access near2 memory).ti.)	US-PGPUB; USPAT	OR	ON	2008/01/07 09:35
S39	12	double near2 data near2 rate and (DRAM.ti. (dynamic near2 random near2 access near2 memory).ti.) and master and slave	US-PGPUB; USPAT	OR	ON	2008/01/07 09:38
S40	2843	master and slave and latch and DRAM	US-PGPUB; USPAT	OR	ON	2008/01/07 09:42
S41	47	master and slave and latch and DRAM.ti.	US-PGPUB; USPAT	OR	ON	2008/01/07 09:44

S42	22	"5940851"	US-PGPUB; USPAT	OR	ON	2008/01/07 10:02
S43	8	"5568428"	US-PGPUB; USPAT	OR	ON	2008/01/07 10:56
S44	3968282	dynamic random access memory device	US-PGPUB; USPAT	OR	ON	2008/01/07 11:01
S45	2557087	dynamic random access memory device.ti.	US-PGPUB; USPAT	OR	ON	2008/01/07 11:02
S46	2557087	dynamic random access memory device.ti.	US-PGPUB; USPAT	OR	ON	2008/01/07 11:02
S47	236	dynamic near2 random near2 access near2 memory near2 device.ti.	US-PGPUB; USPAT	OR	ON	2008/01/07 11:03
S48	1238	(dynamic random access memory).ti.	US-PGPUB; USPAT	ADJ	ON	2008/01/07 11:04
S49	195	dynamic random access memory device.ti.	US-PGPUB; USPAT	ADJ	ON	2008/01/07 11:05
S50	55	dynamic random access memory device.ti. and latch	US-PGPUB; USPAT	ADJ	ON	2008/01/07 11:06
S51	404	dynamic random access memory.ti. and latch	US-PGPUB; USPAT	ADJ	ON	2008/01/07 11:19
S52	1418	327/203,208,210,211,212,218.ccls.	US-PGPUB; USPAT	ADJ	ON	2008/01/14 09:38
S53	409	327/203,208,210,211,212,218.ccls. and master and slave and transistor	US-PGPUB; USPAT	ADJ	ON	2008/01/14 09:38
S54	171	327/203,208,210,211,212,218.ccls. and master and slave and transistor and (pass gate or passgate or transmission gate)	US-PGPUB; USPAT	ADJ	ON	2008/01/14 14:33
S55	5764	signal delay circuit	US-PGPUB; USPAT	ADJ	ON	2008/01/14 16:28

S56	2	(d flip flop).ti. and capacitor	US-PGPUB; USPAT	ADJ	ON	2008/01/15 08:05
S57	46020	latch and capacitor	US-PGPUB; USPAT	ADJ	ON	2008/01/15 08:05
S58	4640	latch and capacitor and "327".clas.	US-PGPUB; USPAT	ADJ	ON	2008/01/15 08:06
S59	172	latch.ti. and capacitor and "327".clas.	US-PGPUB; USPAT	ADJ	ON	2008/01/15 08:07
S60	22	latch.ti. and master and slave and capacitor and "327".clas.	US-PGPUB; USPAT	ADJ	ON	2008/01/15 08:07
S61	172	latch.ti. and capacitor and "327".clas.	US-PGPUB; USPAT	ADJ	ON	2008/01/15 08:08
S62	174	326/93-98.ccls. and 327/202,203,208,210,211,212,218.ccls.	US-PGPUB; USPAT	ADJ	ON	2008/01/15 08:12
S63	407	latch and capacitor and (326/93-98.ccls. or 327/202,203,208,210,211,212,218.ccls.)	US-PGPUB; USPAT	ADJ	ON	2008/01/15 08:12
S64	45361	capacitor same filter same output	US-PGPUB; USPAT	ADJ	ON	2008/01/15 08:25
S65	4975	capacitor same filter same output and "327".clas.	US-PGPUB; USPAT	ADJ	ON	2008/01/15 08:26
S66	698	latch and S65	US-PGPUB; USPAT	ADJ	ON	2008/01/15 08:27
S67	3712	latch.ab. and capacitor	US-PGPUB; USPAT	ADJ	ON	2008/01/15 08:48
S68	2184	latch.ab. and capacitor same output	US-PGPUB; USPAT	ADJ	ON	2008/01/15 08:48
S69	40	BERTHOLD-JORG.in. or JEORGAKOS-GEORG.in. or HENZLER-STEPHAN.in. or SCHMITT- LANDSIEDEL- DORI.s.in.	US-PGPUB; USPAT	ADJ	ON	2008/01/16 14:01

S70	3025	flip flop and filter\$3 capacit\$4	US-PGPUB; USPAT	ADJ	ON	2008/01/17 07:58
S71	4	flip flop.ti. and filter\$3 capacit\$3	US-PGPUB; USPAT	ADJ	ON	2008/01/17 07:59
S72	95	flip flop.ab. and filter\$3 capacit\$3	US-PGPUB; USPAT	ADJ	ON	2008/01/17 08:02
S73	155	latch.ab. and filter\$3 capacit\$3	US-PGPUB; USPAT	ADJ	ON	2008/01/17 08:03
S74	3064	latch and filter\$3 capacit\$3	US-PGPUB; USPAT	ADJ	ON	2008/01/17 08:09
S75	232	latch same filter\$3 capacit\$3	US-PGPUB; USPAT	ADJ	ON	2008/01/17 08:09
S76	28	latch\$3.ti. and filter\$3 capacit\$3	US-PGPUB; USPAT	ADJ	ON	2008/01/17 08:11
S77	370	327/218.ccls.	US-PGPUB; USPAT	ADJ	ON	2008/01/17 08:12
S78	161	327/218.ccls. and capacit\$4	US-PGPUB; USPAT	ADJ	ON	2008/01/17 08:13
S79	11461	latch same capacitor	US-PGPUB; USPAT	ADJ	ON	2008/01/17 08:40
S80	1414	latch same capacitor and ("323.clas" or "327". clas.)	US-PGPUB; USPAT	ADJ	ON	2008/01/17 08:40
S81	0	delay and (capacitor with (potential or ground)) and data "327".clas.	US-PGPUB; USPAT	ADJ	ON	2008/07/02 13:58
S82	2525	delay and (capacitor with (potential or ground)) and data and "327".clas.	US-PGPUB; USPAT	ADJ	ON	2008/07/02 13:58
S83	227	delay and (capacitor with (potential or ground)) and data and "327".clas. and transmission gate	US-PGPUB; USPAT	ADJ	ON	2008/07/02 13:59

S84	314	delay and (capacitor with (potential or ground)) and data and "327".clas. and ("transmission gate" or "pass gate" or "passgate")	US-PGPUB; USPAT	ADJ	ON	2008/07/02 13:59
S85	218	variable delay and (capacitor with (potential or ground)) and data and "327".clas.	US-PGPUB; USPAT	ADJ	ON	2008/07/02 14:21
S86	200	variable delay and (capacitor with (potential or ground)) and data and "327".clas. and clock	US-PGPUB; USPAT	ADJ	ON	2008/07/02 14:28
S87	687	326/93-98.ccls. and capacitor	US-PGPUB; USPAT; USOCR; EPO; JPO	ADJ	ON	2008/07/07 13:38

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